FIG. 1

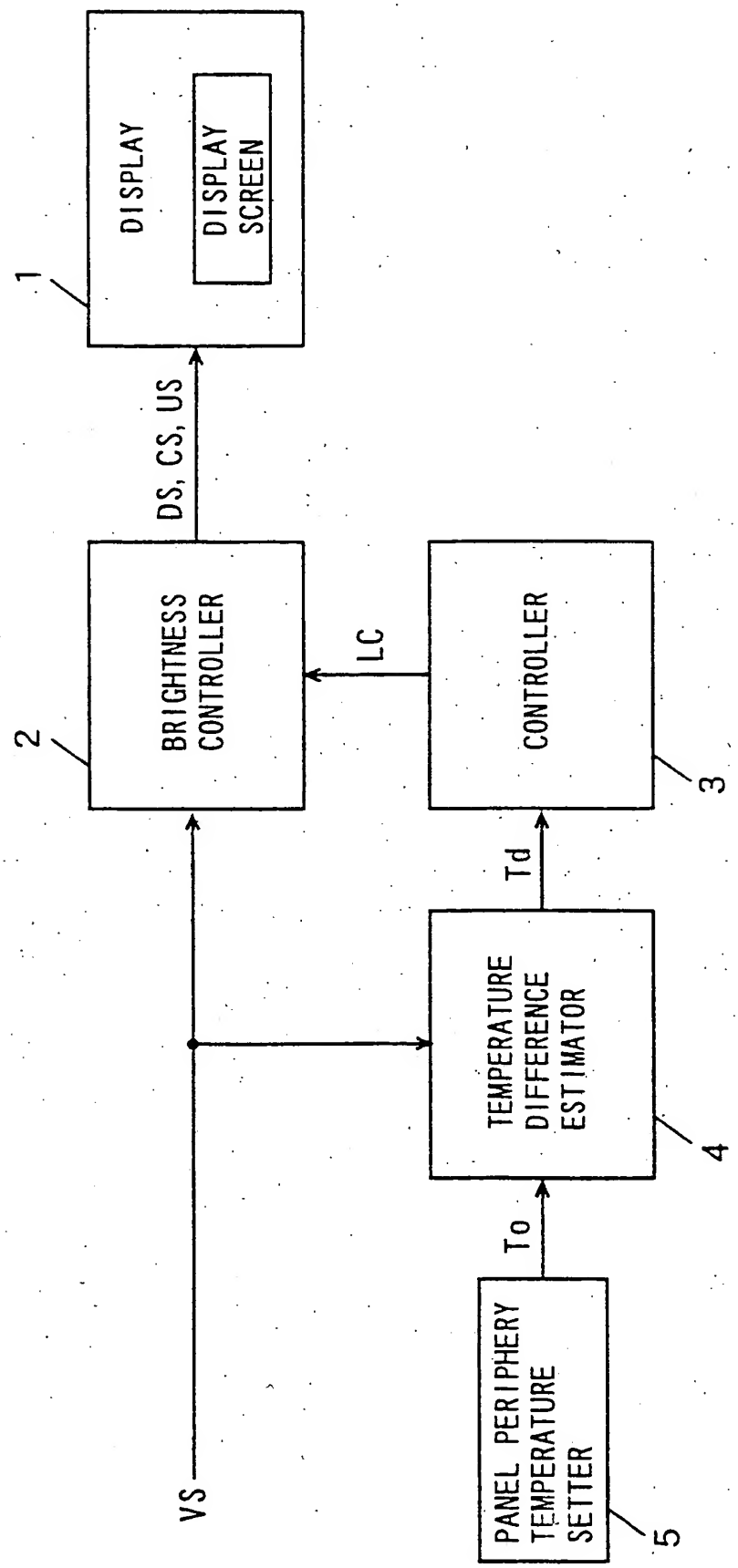

DISPLAY
DISPLAY SCREEN
1
DS, CS, US
BRIGHTNESS CONTROLLER
2
LC
CONTROLLER
3
Td
TEMPERATURE DIFFERENCE ESTIMATOR
4
To
PANEL PERIPHERY TEMPERATURE SETTER
5
VS

FIG. 2

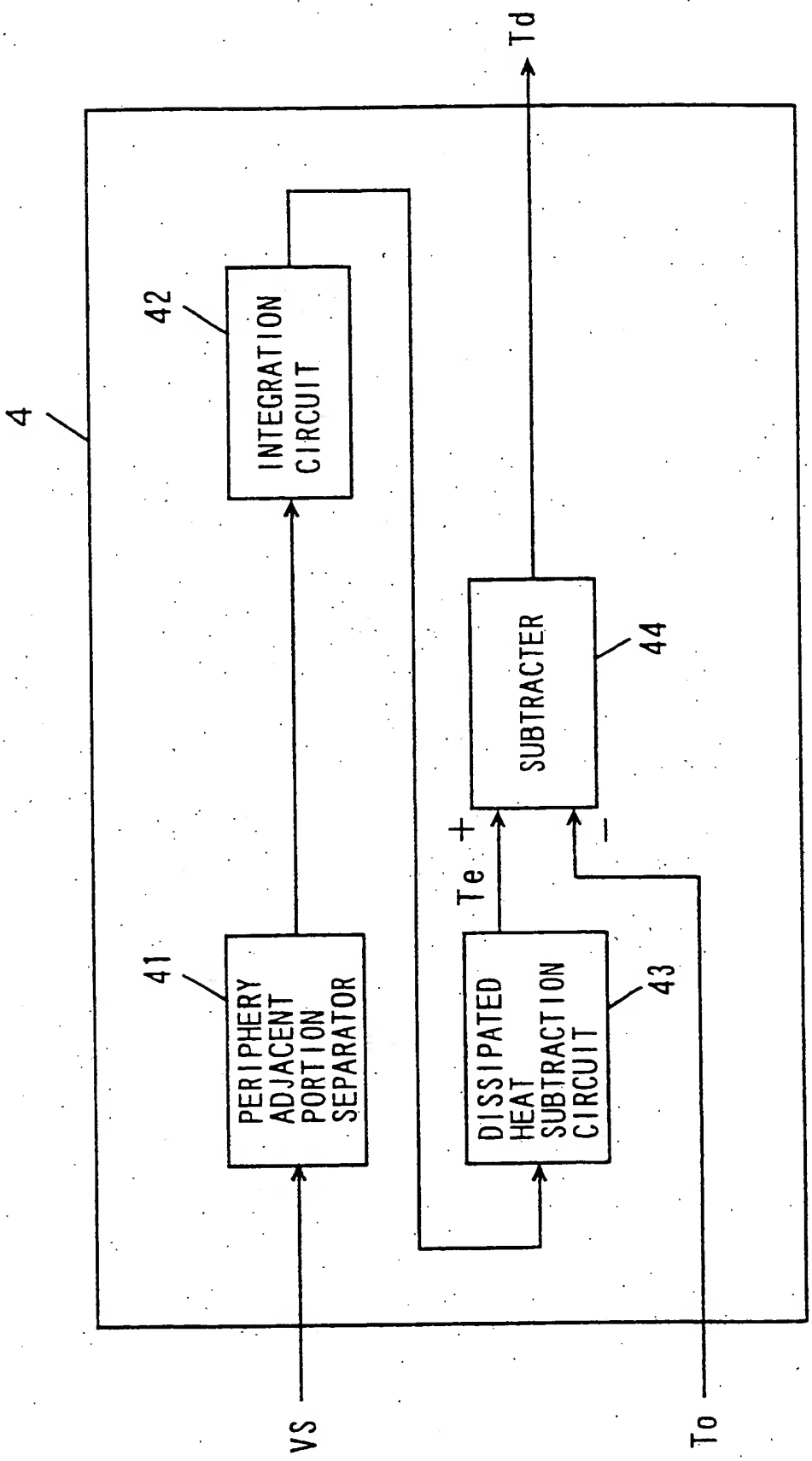

Td
4
42
INTEGRATION CIRCUIT
SUBTRACTER
44
Te
+
−
41
PERIPHERY ADJACENT PORTION SEPARATOR
DISSIPATED HEAT SUBTRACTION CIRCUIT
43
VS
To

FIG. 3

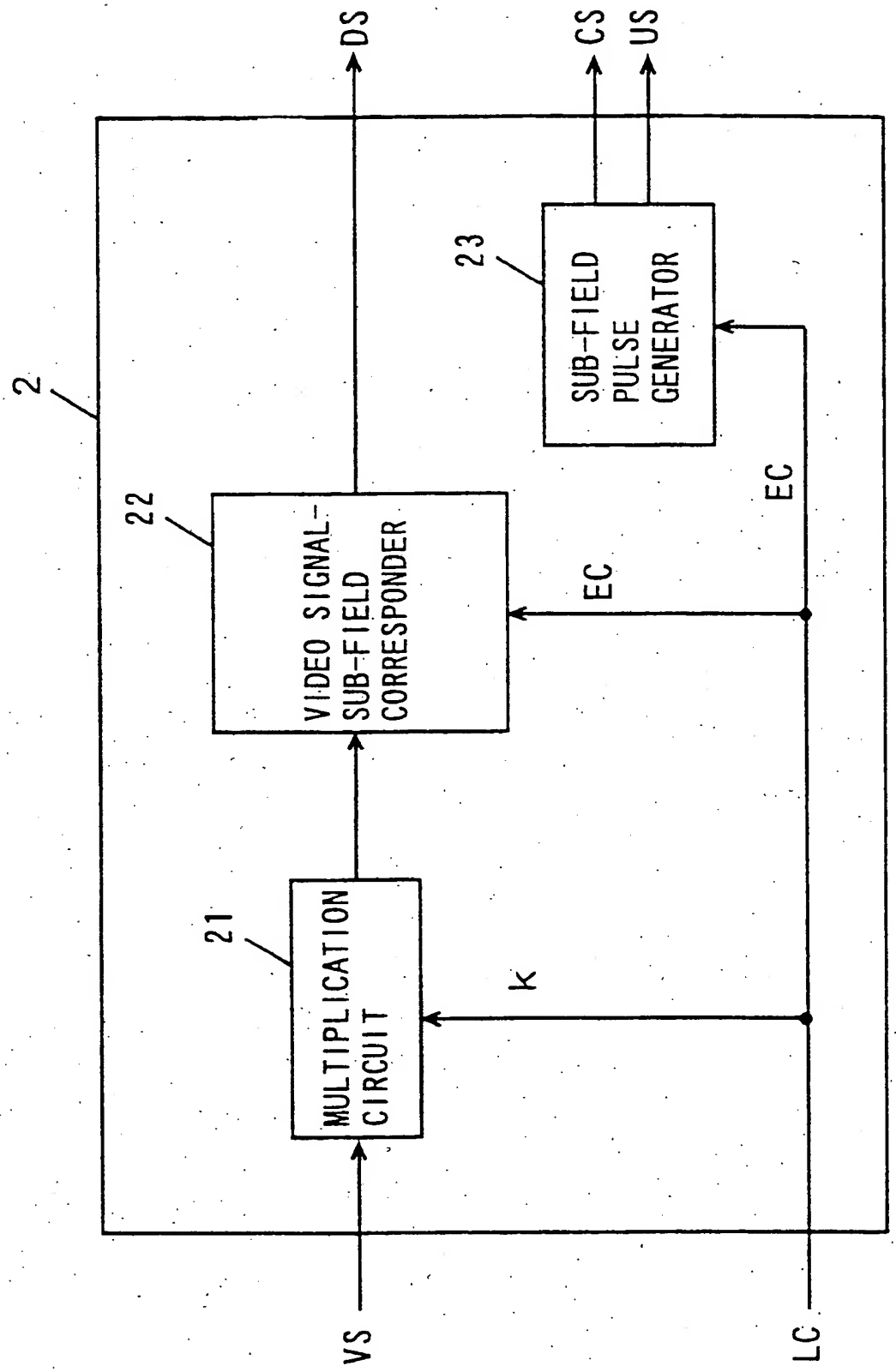

DS
CS
US
2
23
SUB-FIELD PULSE GENERATOR
EC
22
VIDEO SIGNAL-SUB-FIELD CORRESPONDER
EC
21
MULTIPLICATION CIRCUIT
k
VS
LC

FIG. 4

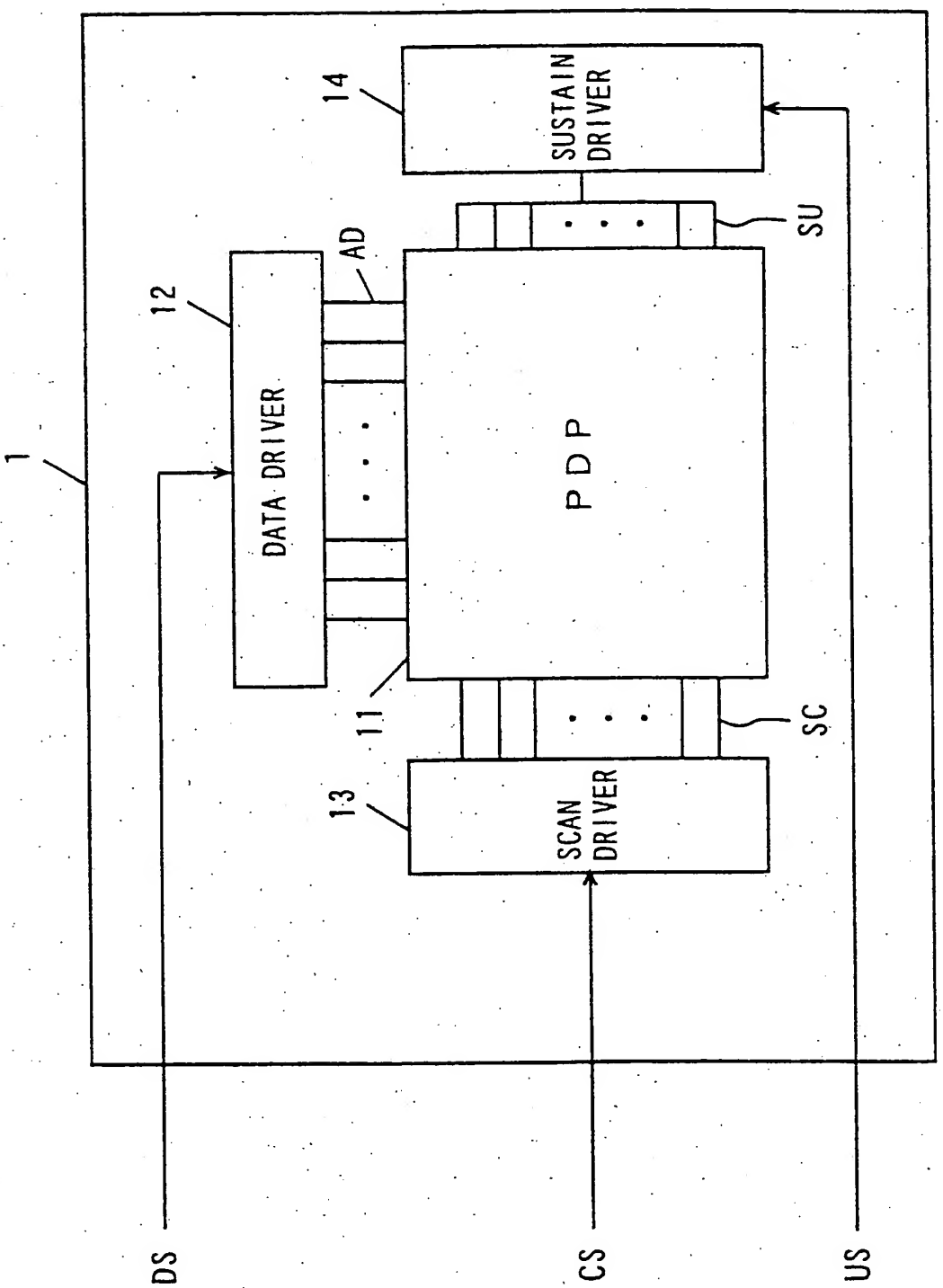

1
14
SUSTAIN DRIVER
SU
AD
12
DATA DRIVER
PDP
11
SC
13
SCAN DRIVER
DS
CS
US

FIG. 5

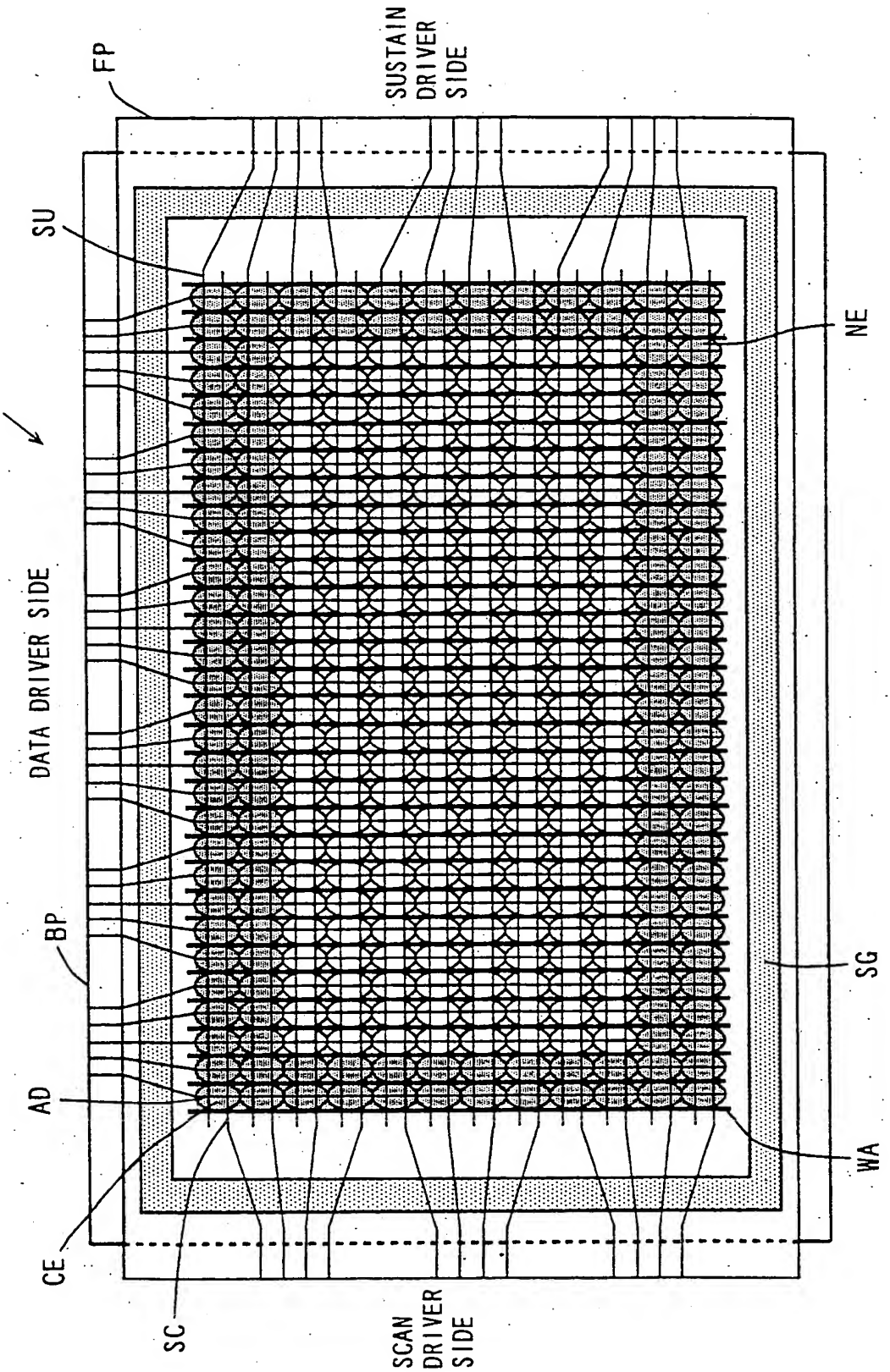

11
FP
SUSTAIN DRIVER SIDE
SU
NE
DATA DRIVER SIDE
BP
SG
AD
WA
CE
SC
SCAN DRIVER SIDE

FIG. 6

GRAY SCALE LEVEL	SF1	SF2	SF3	SF4	SF5	SF6	SF7	SF8
0								
1	○							
2		○						
3	○	○						
4～7	SAME AS 0～3		○					
8～15	SAME AS 0～7			○				
16～31	SAME AS 0～15				○			
32～63	SAME AS 0～31					○		
64～127	SAME AS 0～63						○	
128～255	SAME AS 0～127							○

FIG. 7

NUMBER OF LIGHT EMITTING PULSES

LIGHT EMITTING PULSE CONTROL SIGNAL	SF1	SF2	SF3	SF4	SF5	SF6	SF7	SF8	TOTAL
LIGHT EMITTING FORMAT A	5	10	20	40	80	160	320	640	1275
LIGHT EMITTING FORMAT B	4	8	16	32	64	128	256	512	1020
LIGHT EMITTING FORMAT C	3	6	12	24	48	96	192	384	765
LIGHT EMITTING FORMAT D	2	4	8	16	32	64	128	256	510
LIGHT EMITTING FORMAT E	1	2	4	8	16	32	64	128	255

FIG. 8

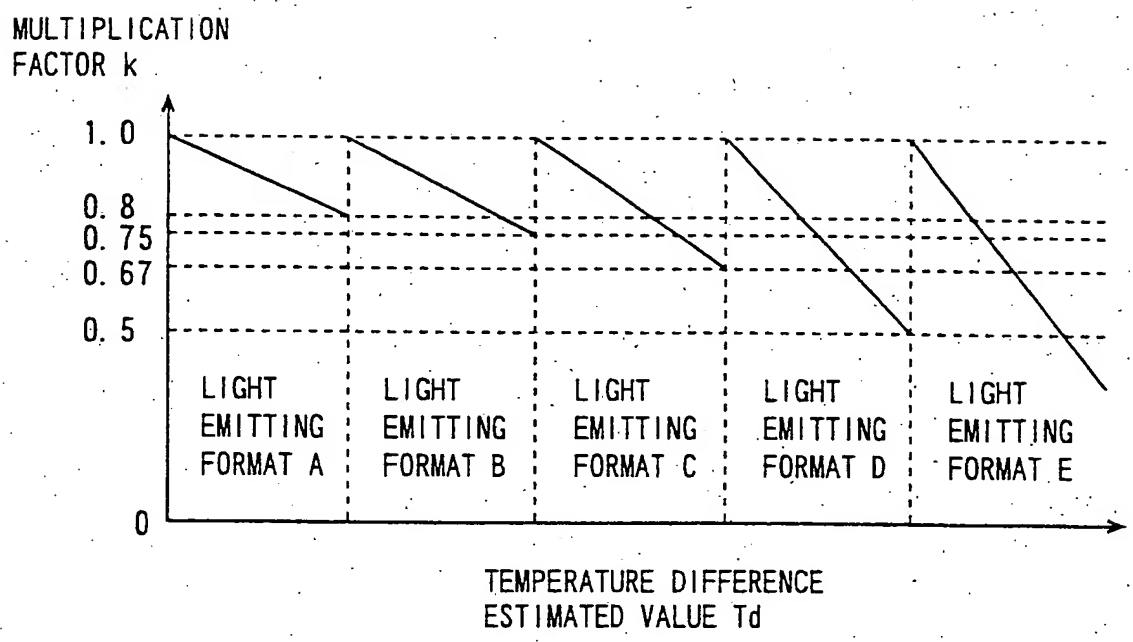

MULTIPLICATION FACTOR k
1.0
0.8
0.75
0.67
0.5
0
LIGHT EMITTING FORMAT A
LIGHT EMITTING FORMAT B
LIGHT EMITTING FORMAT C
LIGHT EMITTING FORMAT D
LIGHT EMITTING FORMAT E
TEMPERATURE DIFFERENCE ESTIMATED VALUE Td

FIG. 9

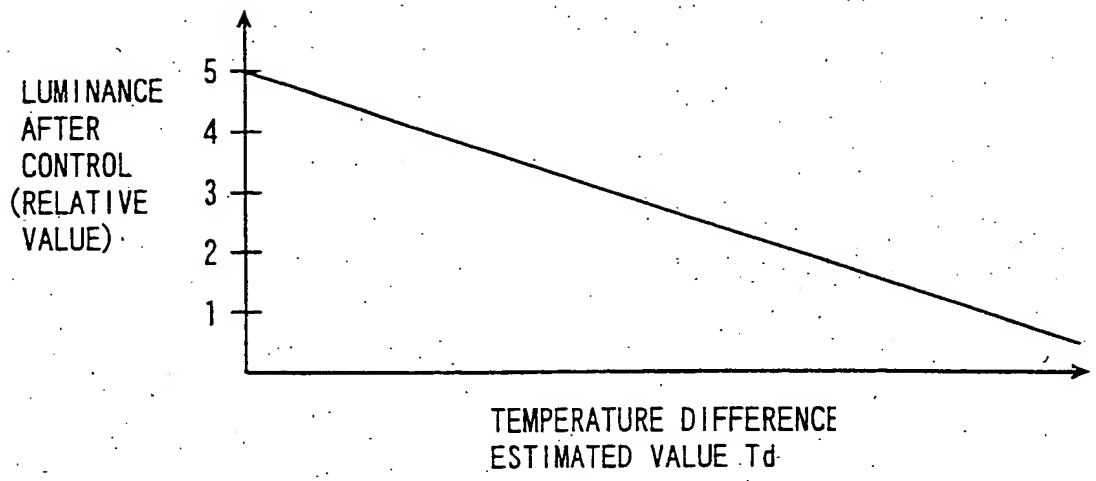

LUMINANCE AFTER CONTROL (RELATIVE VALUE)
5
4
3
2
1
TEMPERATURE DIFFERENCE ESTIMATED VALUE Td

FIG. 10

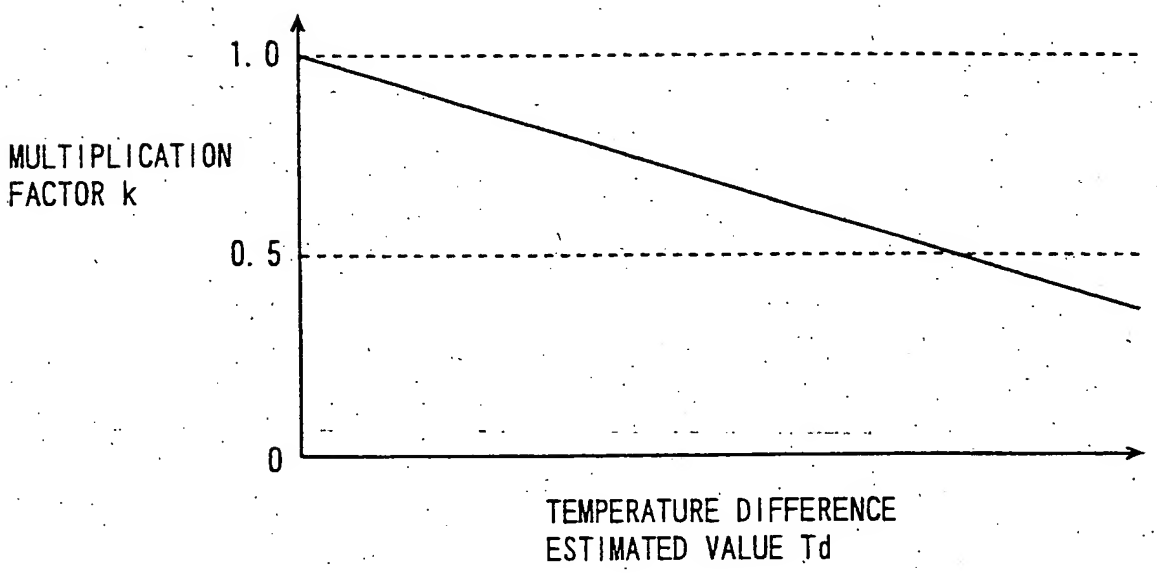
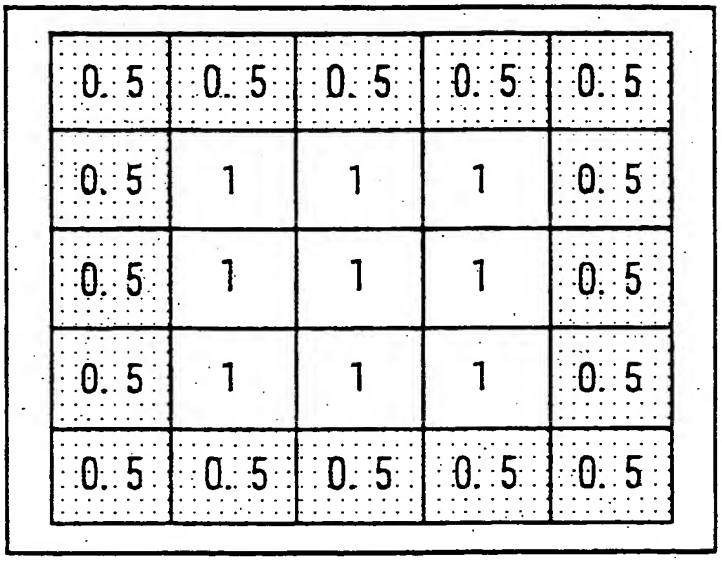
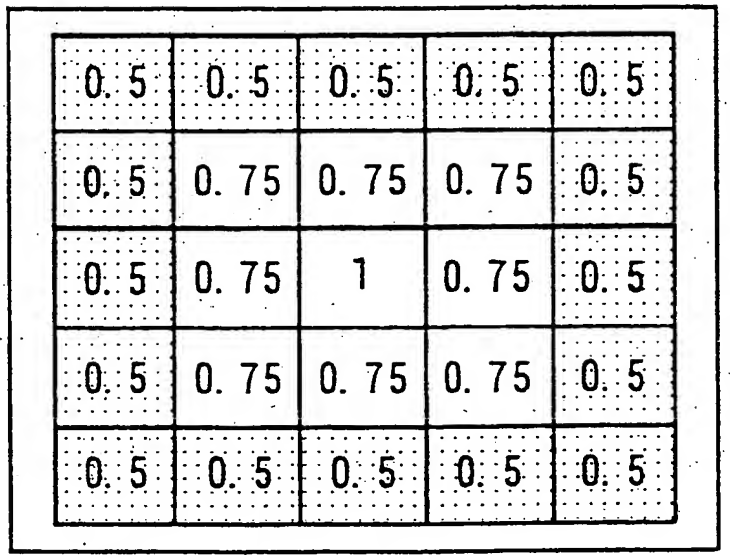

1.0
MULTIPLICATION FACTOR k
0.5
0
TEMPERATURE DIFFERENCE ESTIMATED VALUE Td

FIG. 11



0.5	0.5	0.5	0.5	0.5
0.5	1	1	1	0.5
0.5	1	1	1	0.5
0.5	1	1	1	0.5
0.5	0.5	0.5	0.5	0.5

FIG. 12



0.5	0.5	0.5	0.5	0.5
0.5	0.75	0.75	0.75	0.5
0.5	0.75	1	0.75	0.5
0.5	0.75	0.75	0.75	0.5
0.5	0.5	0.5	0.5	0.5

FIG. 13

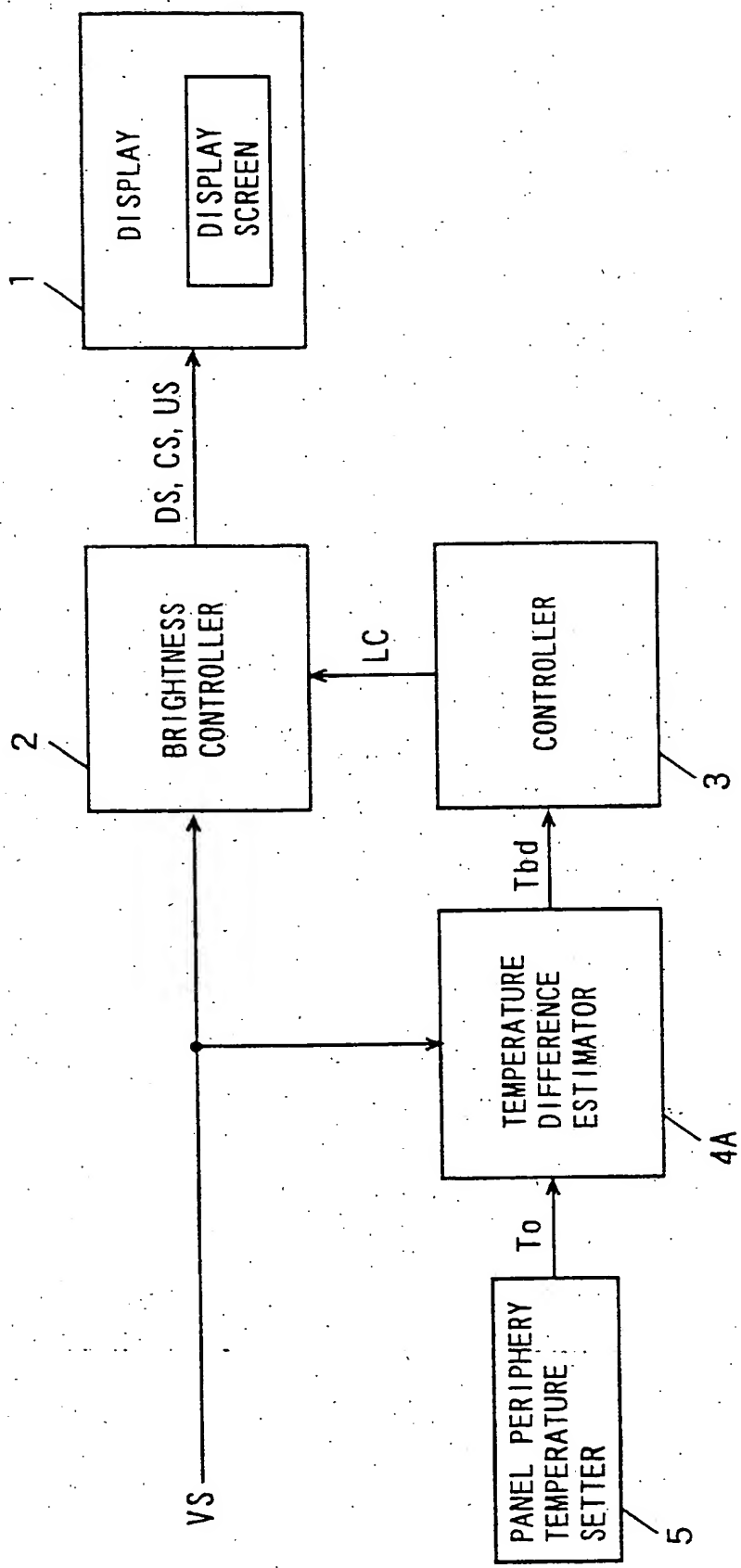

DISPLAY
DISPLAY SCREEN
1
DS, CS, US
BRIGHTNESS CONTROLLER
2
LC
CONTROLLER
3
Tbd
TEMPERATURE DIFFERENCE ESTIMATOR
4A
To
PANEL PERIPHERY TEMPERATURE SETTER
5
VS

FIG. 14

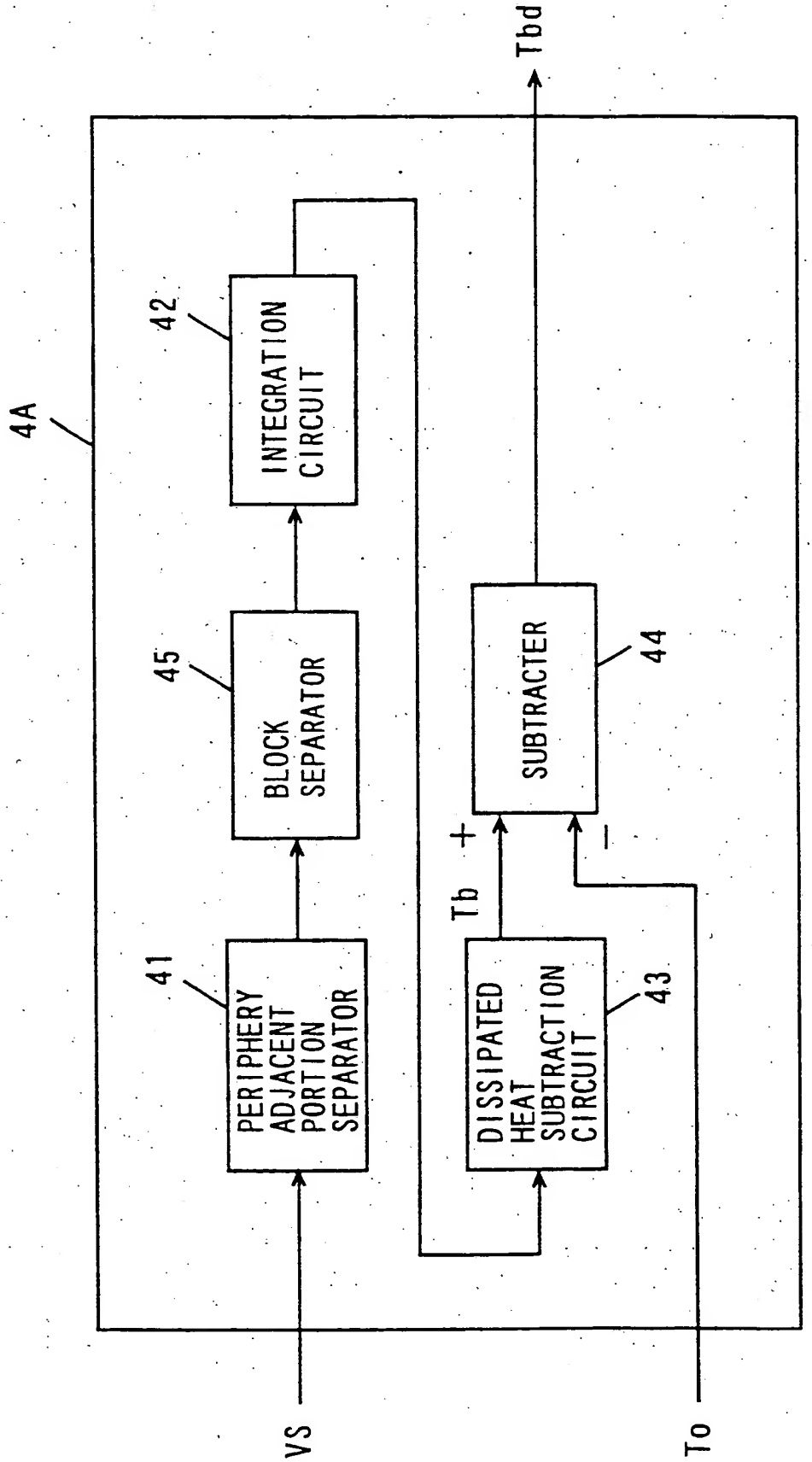

Tbd
4A
42
INTEGRATION CIRCUIT
45
BLOCK SEPARATOR
41
PERIPHERY ADJACENT PORTION SEPARATOR
SUBTRACTER
44
+
−
Tb
DISSIPATED HEAT SUBTRACTION CIRCUIT
43
VS
To

FIG. 15

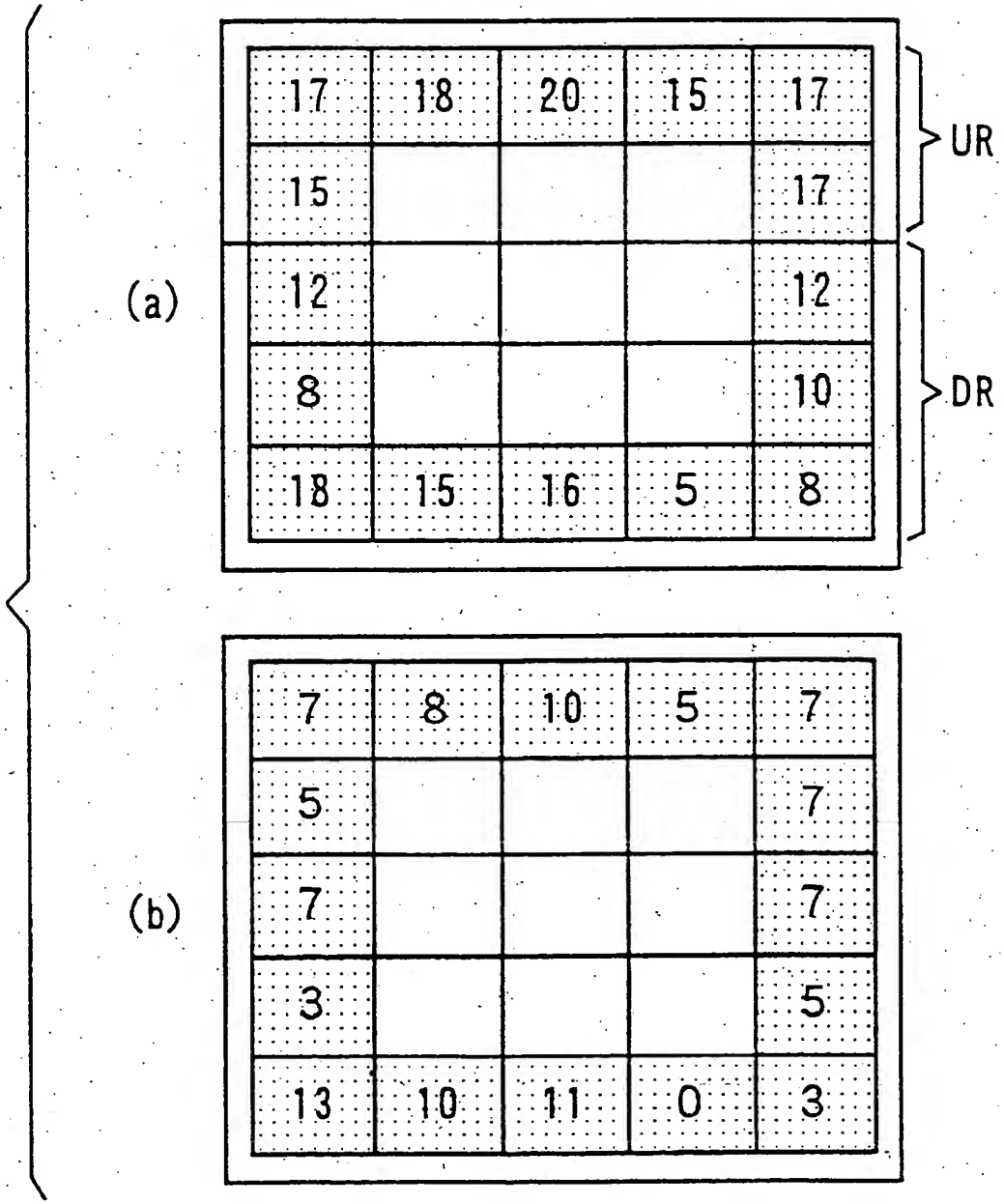

(a)
17 18 20 15 17
15 17
12 12
8 10
18 15 16 5 8
UR
DR
(b)
7 8 10 5 7
5 7
7 7
3 5
13 10 11 0 3

FIG. 16

(a)

0	0	20	0	0
0				0
20				20
0				0
0	0	20	0	0

(b)

1	1	0.5	1	1
1				1
0.5				0.5
1				1
1	1	0.5	1	1

FIG. 17

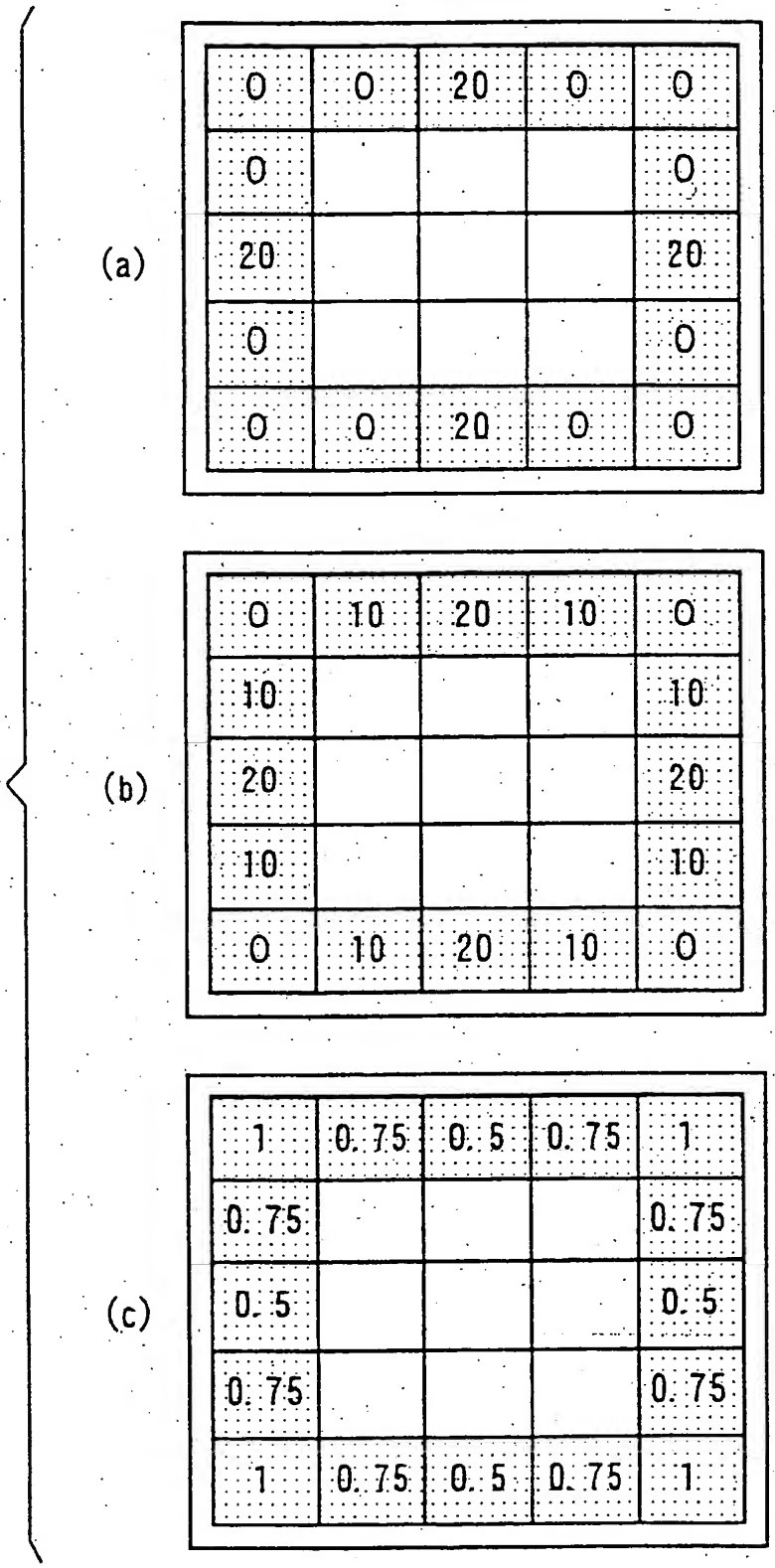

(a)
0 0 20 0 0
0 0
20 20
0 0
0 0 20 0 0
(b)
0 10 20 10 0
10 10
20 20
10 10
0 10 20 10 0
(c)
1 0.75 0.5 0.75 1
0.75 0.75
0.5 0.5
0.75 0.75
1 0.75 0.5 0.75 1

FIG. 18

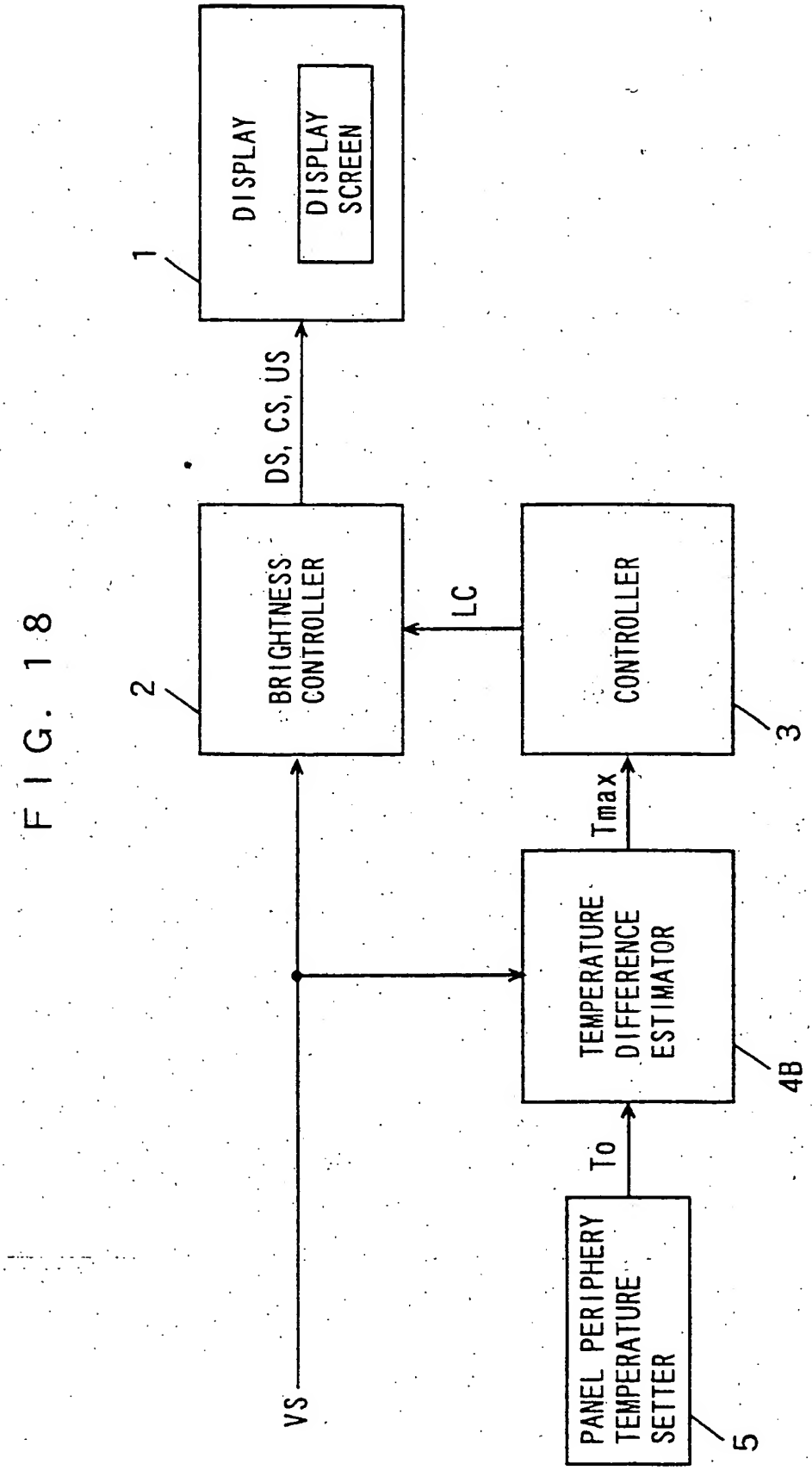

DISPLAY
DISPLAY SCREEN
1
DS, CS, US
BRIGHTNESS CONTROLLER
2
LC
CONTROLLER
3
Tmax
TEMPERATURE DIFFERENCE ESTIMATOR
4B
To
PANEL PERIPHERY TEMPERATURE SETTER
5
VS

FIG. 19

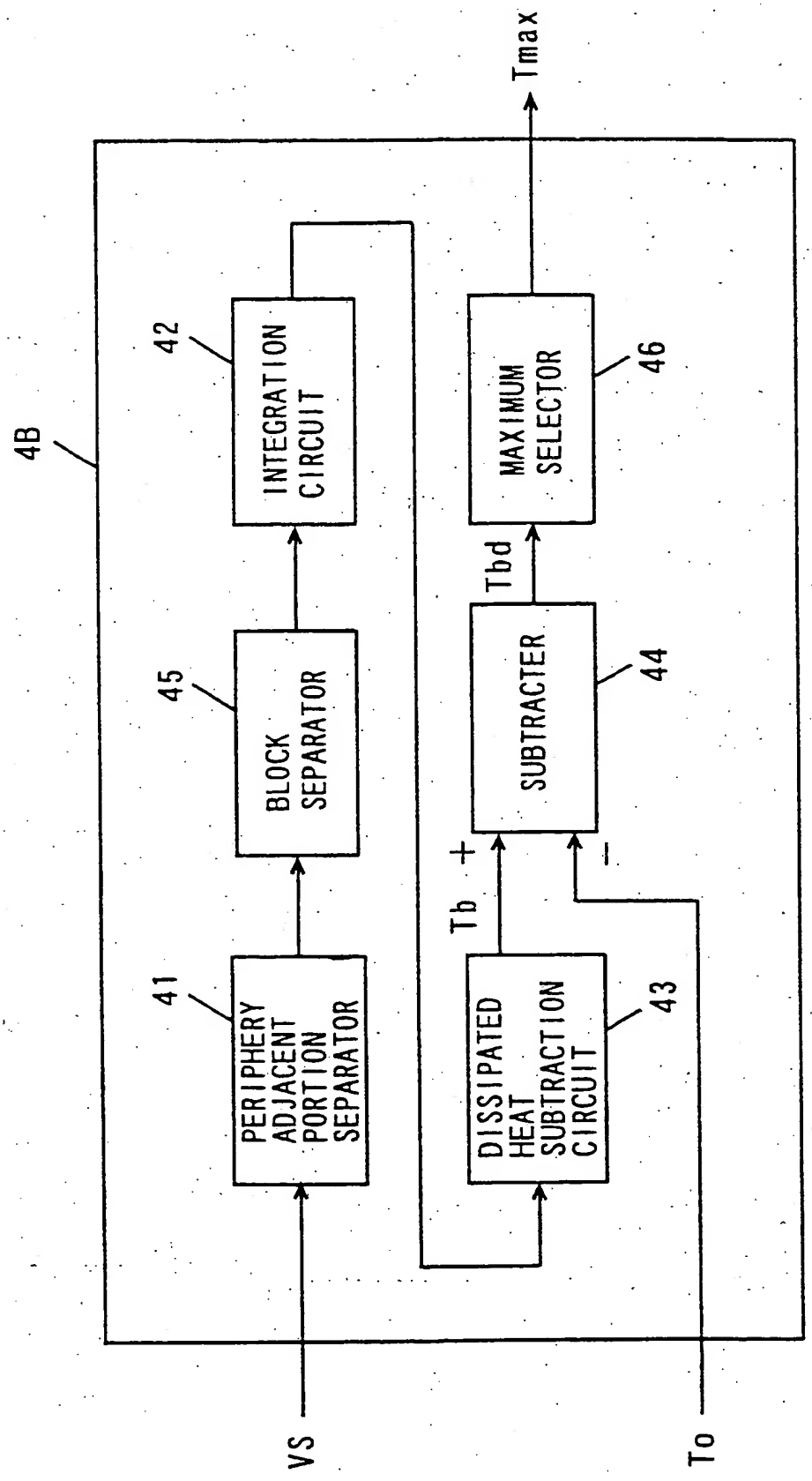

4B
41
PERIPHERY ADJACENT PORTION SEPARATOR
42
INTEGRATION CIRCUIT
43
DISSIPATED HEAT SUBTRACTION CIRCUIT
44
SUBTRACTER
45
BLOCK SEPARATOR
46
MAXIMUM SELECTOR
Tmax
Tbd
Tb
+
−
VS
To

FIG. 20

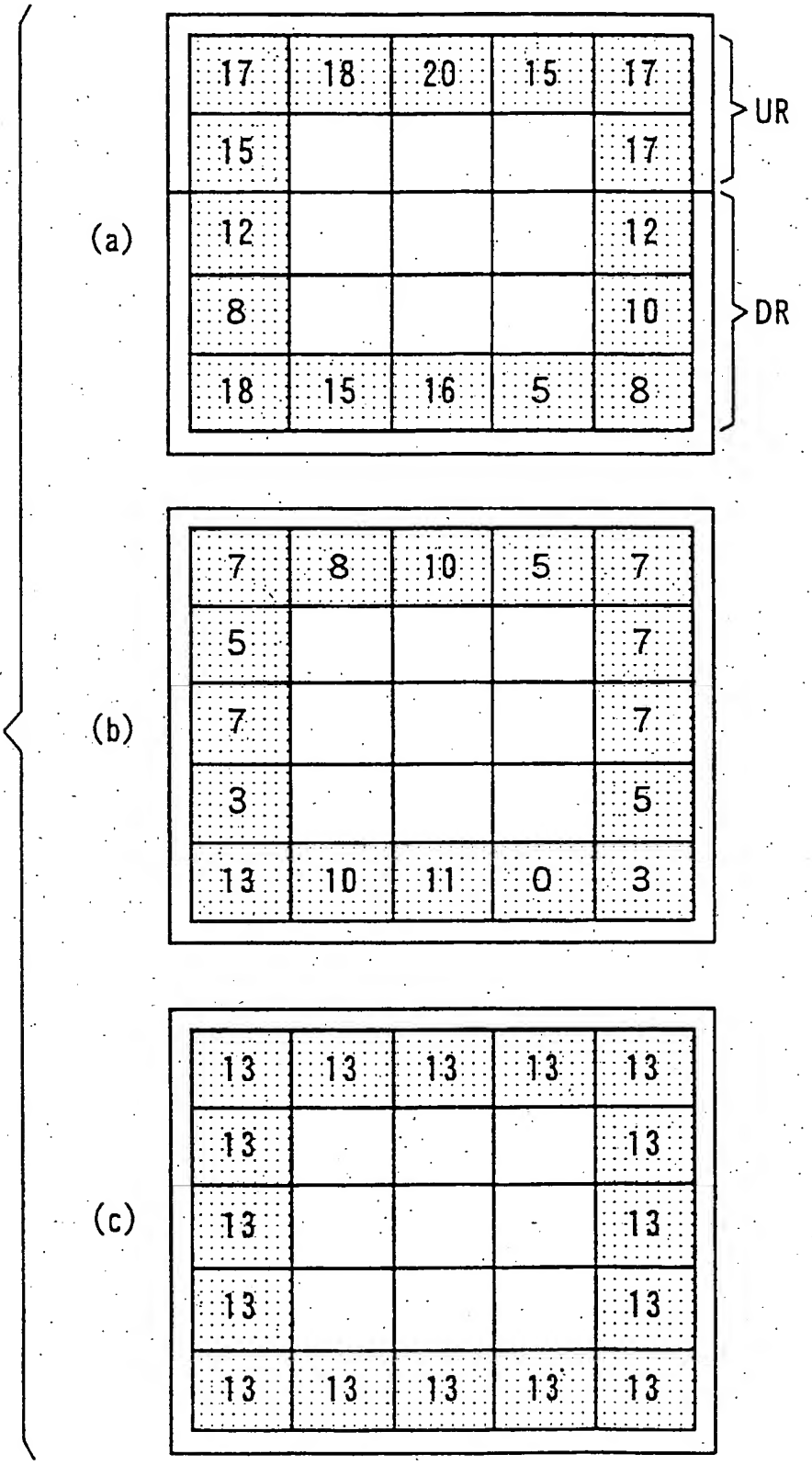
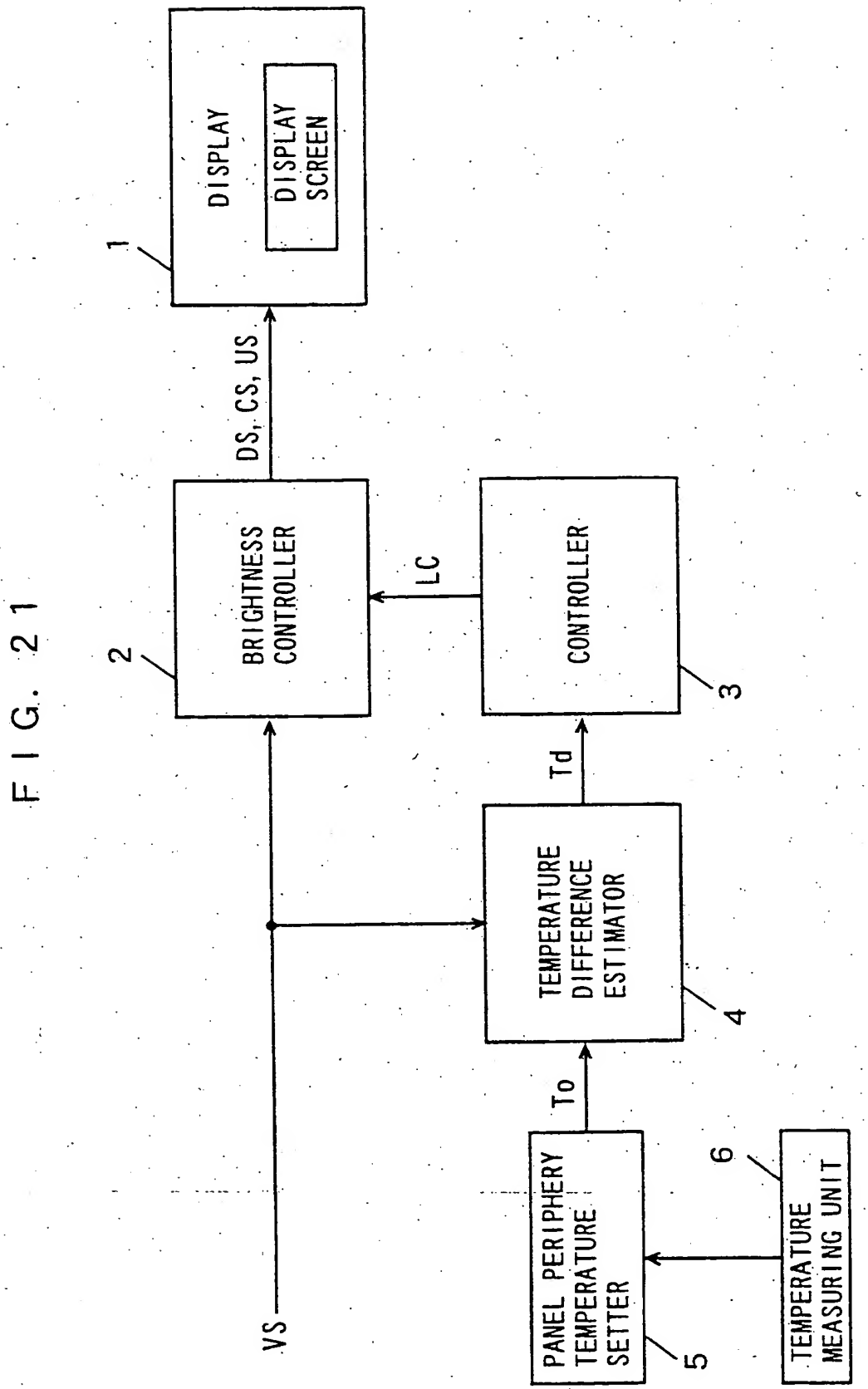

(a)
17 18 20 15 17
15 17
12 12
8 10
18 15 16 5 8
UR
DR
(b)
7 8 10 5 7
5 7
7 7
3 5
13 10 11 0 3
(c)
13 13 13 13 13
13 13
13 13
13 13
13 13 13 13 13

FIG. 21


DISPLAY
DISPLAY SCREEN
1
DS, CS, US
BRIGHTNESS CONTROLLER
2
LC
CONTROLLER
3
Td
TEMPERATURE DIFFERENCE ESTIMATOR
4
To
PANEL PERIPHERY TEMPERATURE SETTER
5
6
TEMPERATURE MEASURING UNIT
VS